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FACSIMILE TRANSMITTAL

Date: June 21, 2005

To: Examiner John J. Tabone, Jr.

Organization: U.S. Patent and Trademark Office

Fax Number: (703) 872-9306

Phone Number: (571) 272-3827

From: Kelvin M. Vivian, Reg. No. 53,727

Re: 10/016,449 - Amendment Response
(IBM - 2280P)

This is page 1 of 13 pages.

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TRANSMITTAL FORM

Attorney Docket No.
RPS920010127US1
2280P

In re the application Robert T. BAILIS, et al.

Confirmation No: 5286

Serial No: 10/016,449

Group Art Unit: 2133

Filed: December 10, 2001

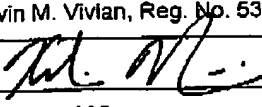
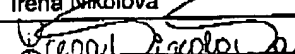
Examiner: Tabone Jr., John J.

For: Method and System for Use of a Field Programmable Gate Array (FPGA) Function within an
Application Specific Integrated Circuit (ASIC) to Enable Creati

ENCLOSURES (check all that apply)			
<input checked="" type="checkbox"/>	Amendment/Reply	<input type="checkbox"/>	Assignment and Recordation Cover Sheet
<input type="checkbox"/>	After Final	<input type="checkbox"/>	Part B-Issue Fee Transmittal
<input type="checkbox"/>	Information disclosure statement	<input type="checkbox"/>	Letter to Draftsman
<input type="checkbox"/>	Form 1449	<input type="checkbox"/>	Drawings
<input type="checkbox"/>	(X) Copies of References	<input type="checkbox"/>	Petition
<input type="checkbox"/>	Extension of Time Request *	<input type="checkbox"/>	Fee Address Indication Form
<input type="checkbox"/>	Express Abandonment	<input type="checkbox"/>	Terminal Disclaimer
<input type="checkbox"/>	Certified Copy of Priority Doc	<input type="checkbox"/>	Power of Attorney and Revocation of Prior Powers
<input type="checkbox"/>	Response to Incomplete Appin	<input type="checkbox"/>	Change of Correspondence Address
<input type="checkbox"/>	Response to Missing Parts	*Extension of Term: Pursuant to 37 CFR 1.136, Applicant petitions the Commissioner to extend the time for response for xxxxxx month(s), from to .	
<input type="checkbox"/>	Executed Declaration by Inventor(s)		

CLAIMS					
FOR	Claims Remaining After Amendment	Highest # of Claims Previously Paid For	Extra Claims	RATE	FEE
Total Claims	13	20	0	\$ 50.00	\$ 0.00
Independent Claims	2	3	0	\$200.00	\$ 0.00
				Total Fees	\$ 0.00

METHOD OF PAYMENT	
<input type="checkbox"/>	Check no. _____ in the amount of \$ _____ is enclosed for payment of fees.
<input checked="" type="checkbox"/>	Charge any fees or credit any overpayment to Deposit Account No. <u>50-0563</u> (IBM Corporation)

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Attorney Name	Kelvin M. Vivian, Reg. No. 53,727
Signature	
Date	June 21, 2005
CERTIFICATE OF FACSIMILE	
I hereby certify that this correspondence is being faxed to Examiner John J. Tabone, Jr. (703-872-9306) at the USPTO, on June 21, 2005.	
Type or printed name	Irena Nikolova
Signature	

CERTIFICATE OF FACSIMILE

I hereby certify that this correspondence is being faxed to Examiner John J. Tabone, Jr. (703-872-9306) at the USPTO, on June 21, 2005


Irena Nikolova

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In Re Application of:

Date: June 21, 2005

Robert T. BAILIS, et al.

Confirmation No: 5286

JUN 21 2005

Serial No: 10/016,449

Group Art Unit: 2133

Filed: December 10, 2001

Examiner: John J. Tabone, Jr.

For: METHOD AND SYSTEM FOR USE OF A FIELD PROGRAMMABLE GATE
ARRAY (FPGA) FUNCTION WITHIN AN APPLICATION SPECIFIC
INTEGRATED CIRCUIT (ASIC) TO ENABLE CREATION OF A DEBUGGER
CLIENT WITHIN THE ASIC

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT IN REPLY TO ACTION OF MARCH 23, 2005

In response to the Office Action dated March 23, 2005, please amend the above-identified application as follows:

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

Remarks/Arguments begin on page 6 of this paper.